

1099684

TITLE: CO-PLANARITY AND TOP-DOWN EXAMINATION METHOD AND OPTICAL MODULE
FOR ELECTRONIC LEADED COMPONENTS
INVENTORS NAME: Bradley L. Quist
SERIAL NO.: 10/099,684

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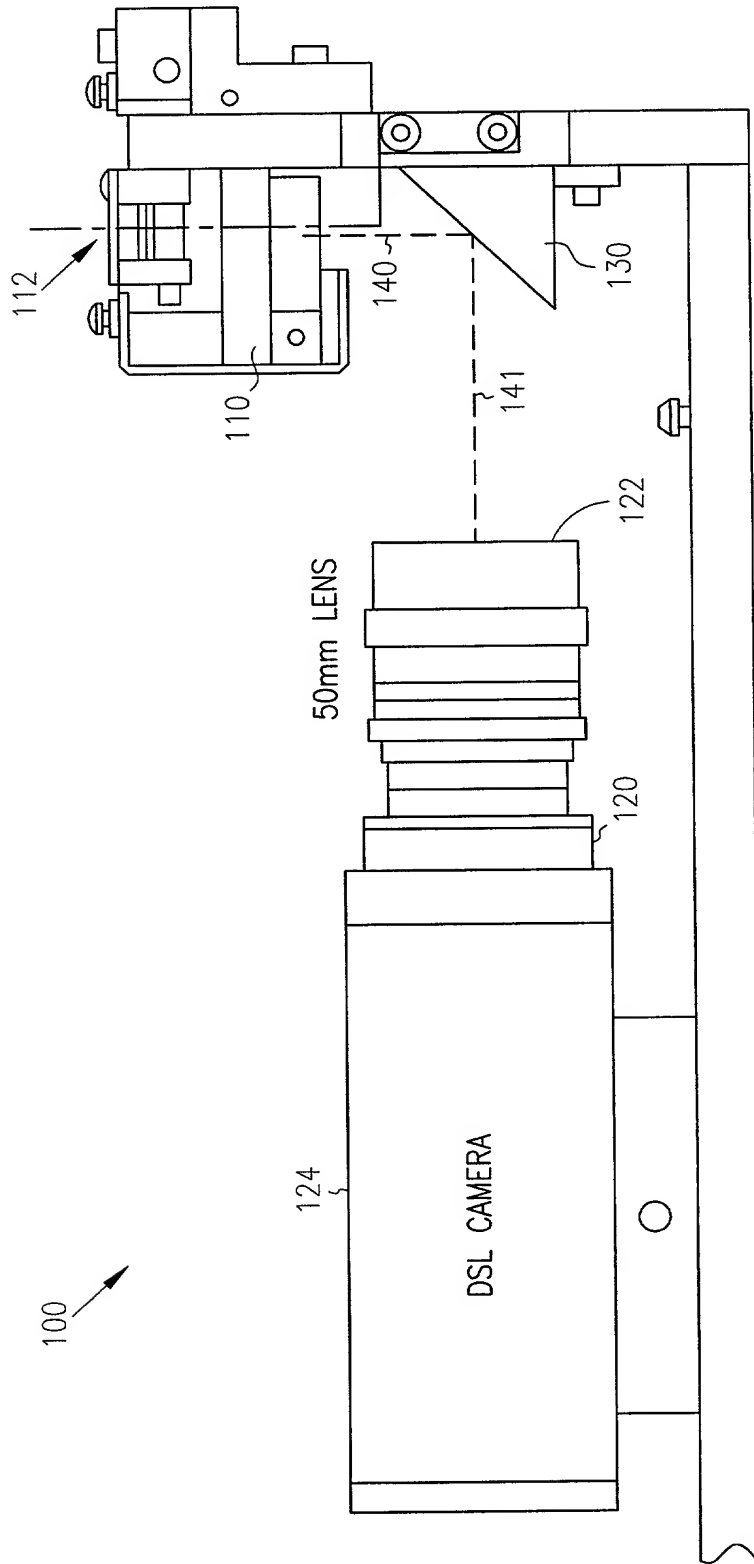


FIG. 1

[illegible]

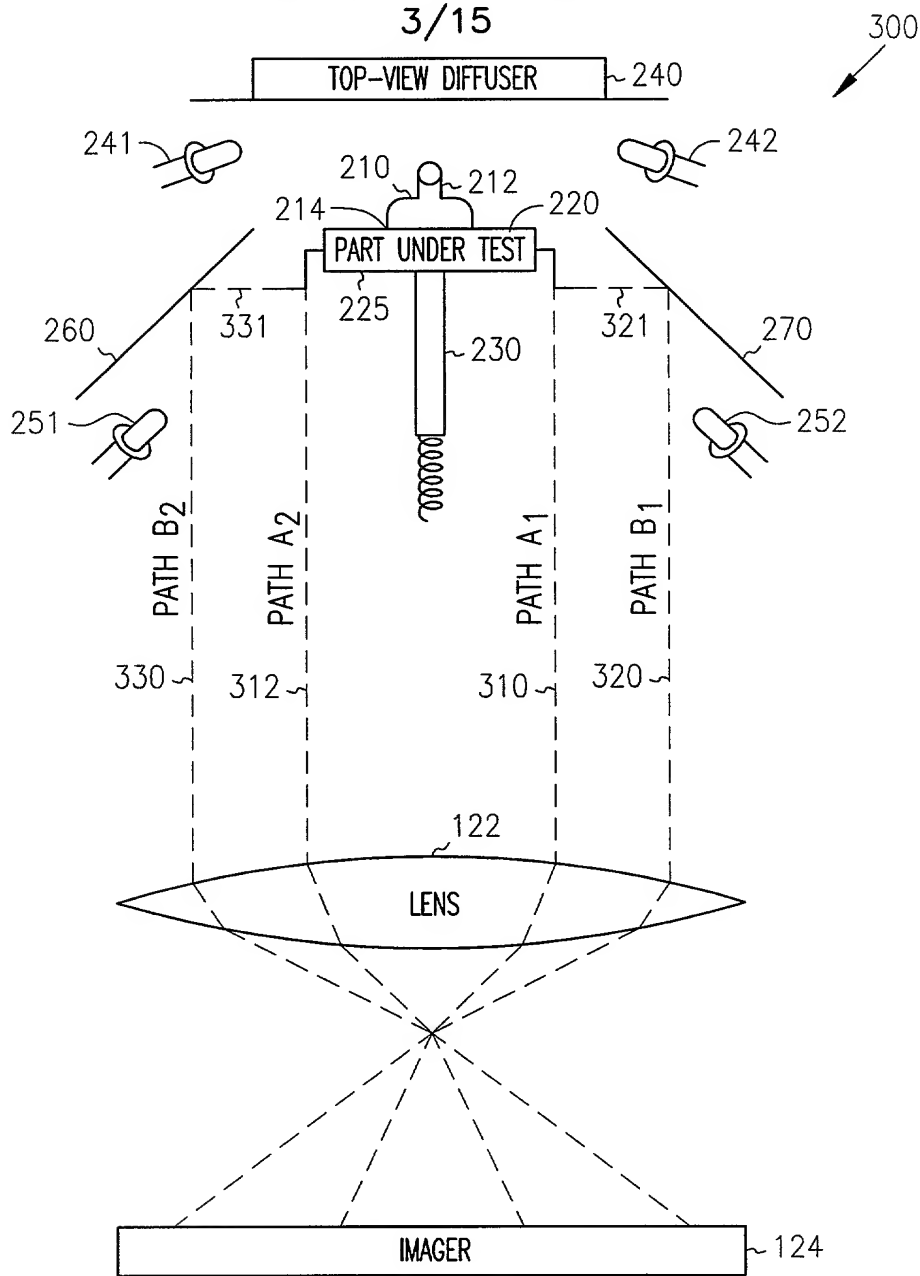


FIG. 3A

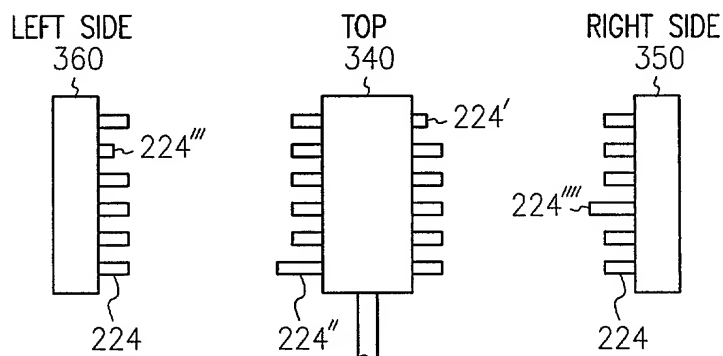


FIG. 3B

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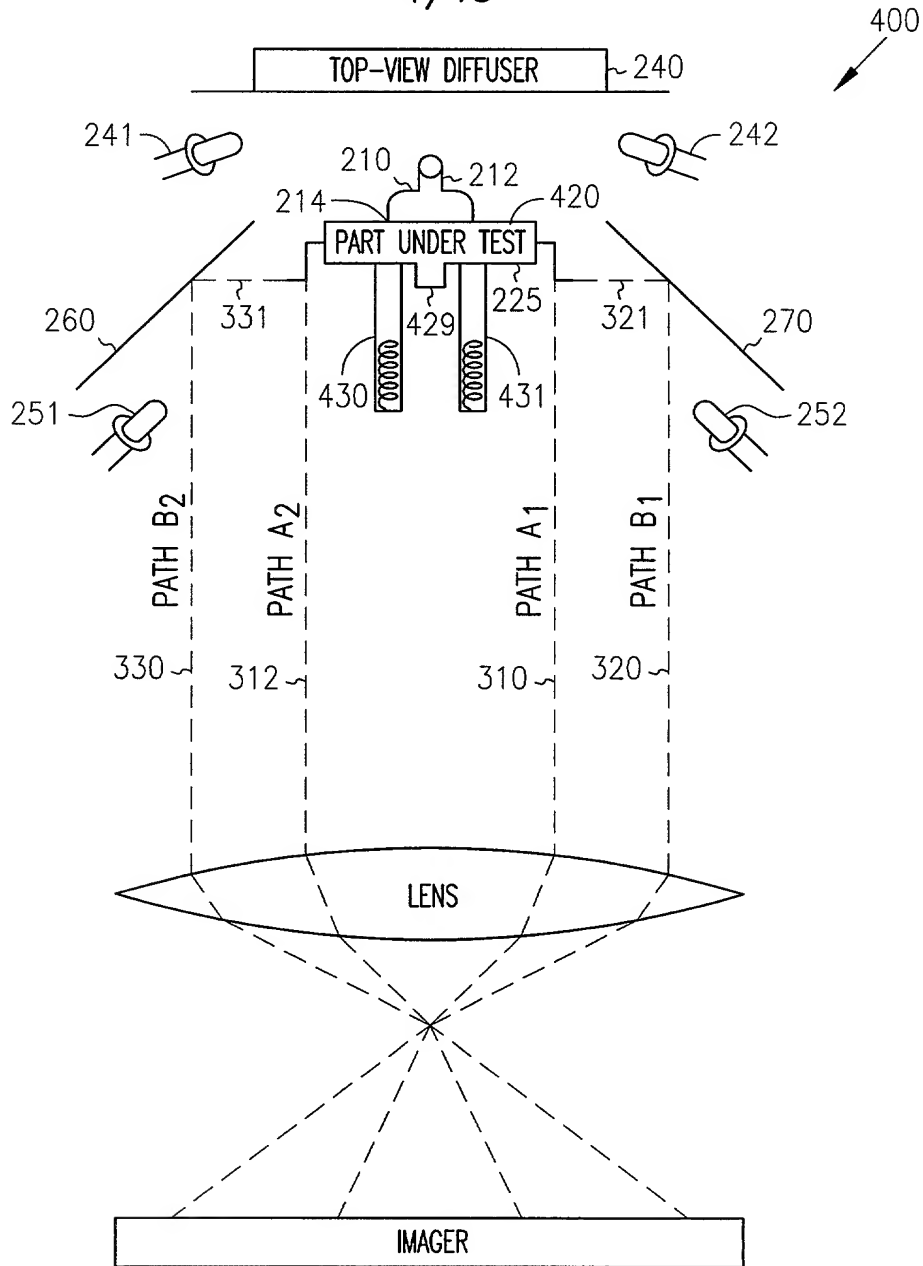


FIG. 4A

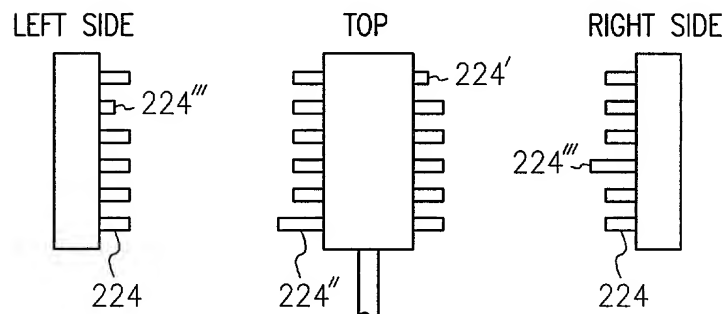


FIG. 4B

10099684-101500

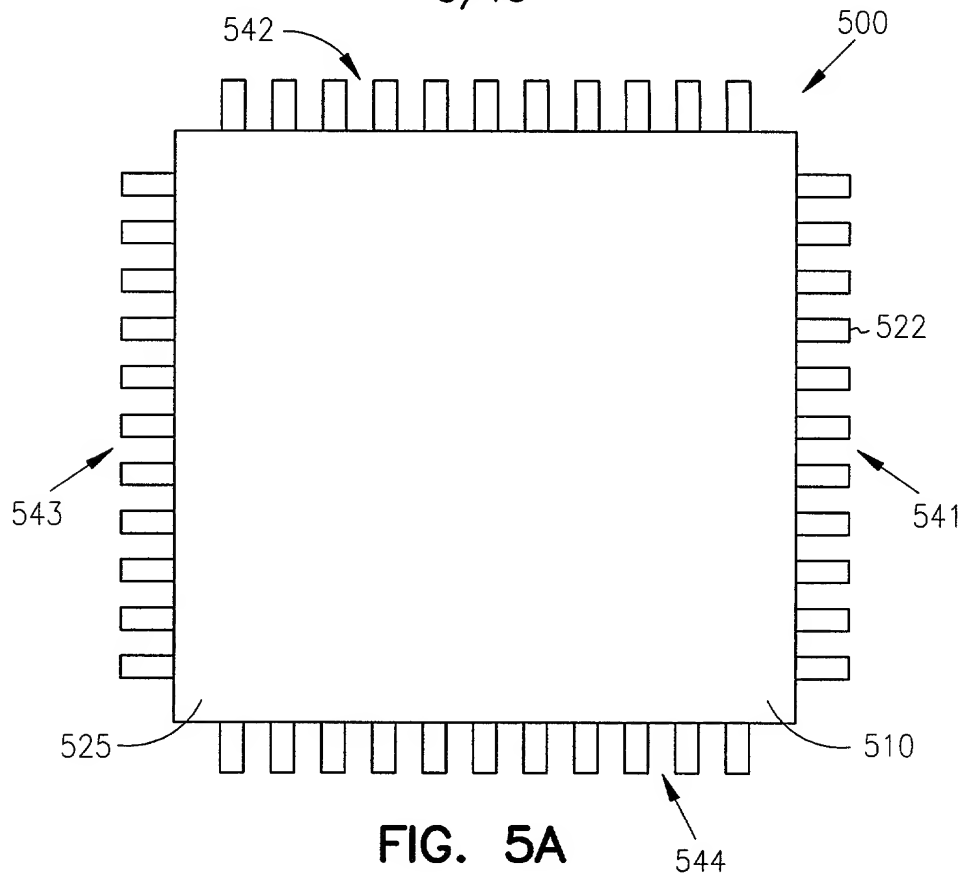


FIG. 5A

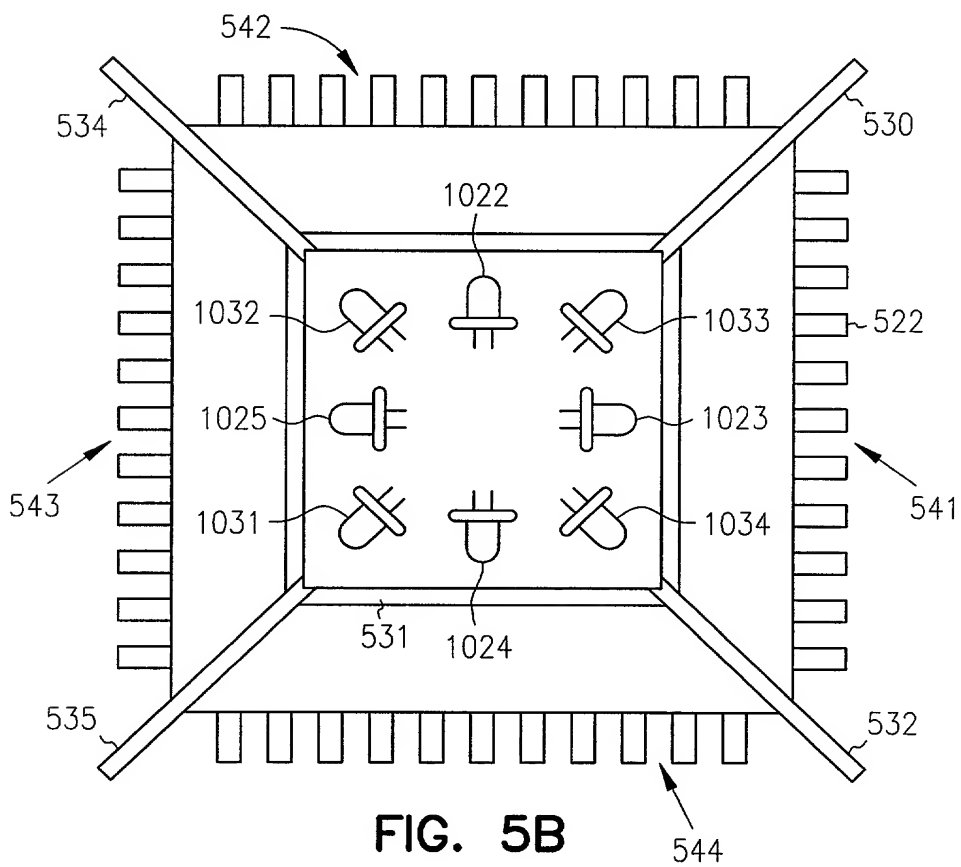


FIG. 5B

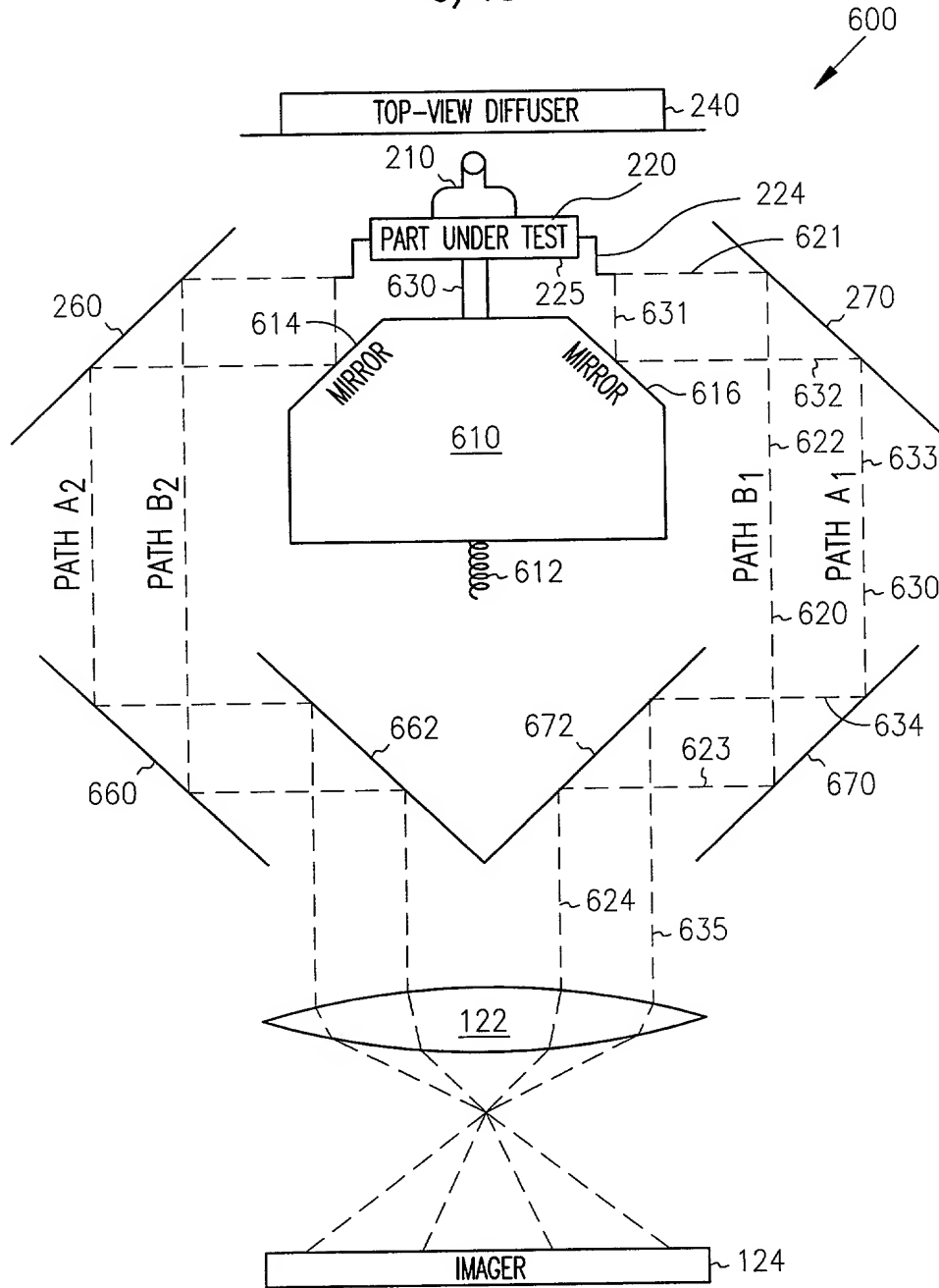


FIG. 6A

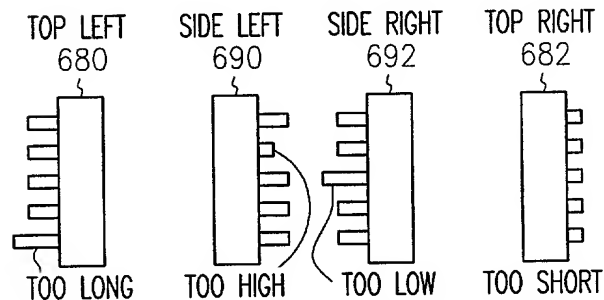


FIG. 6B

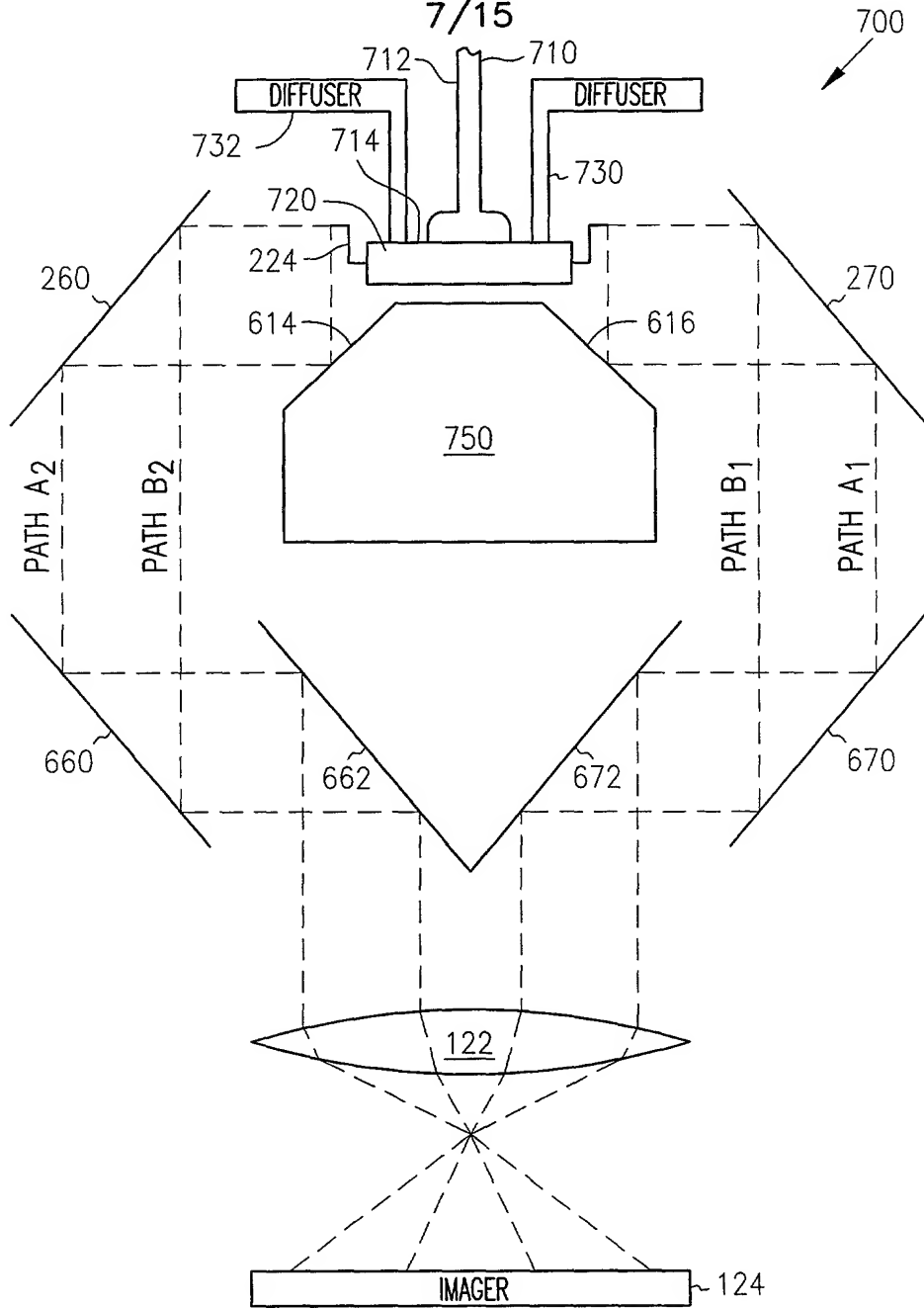


FIG. 7A

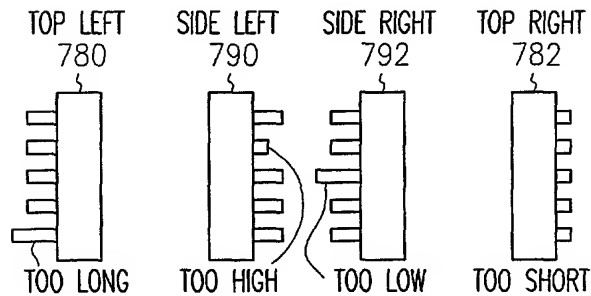


FIG. 7B

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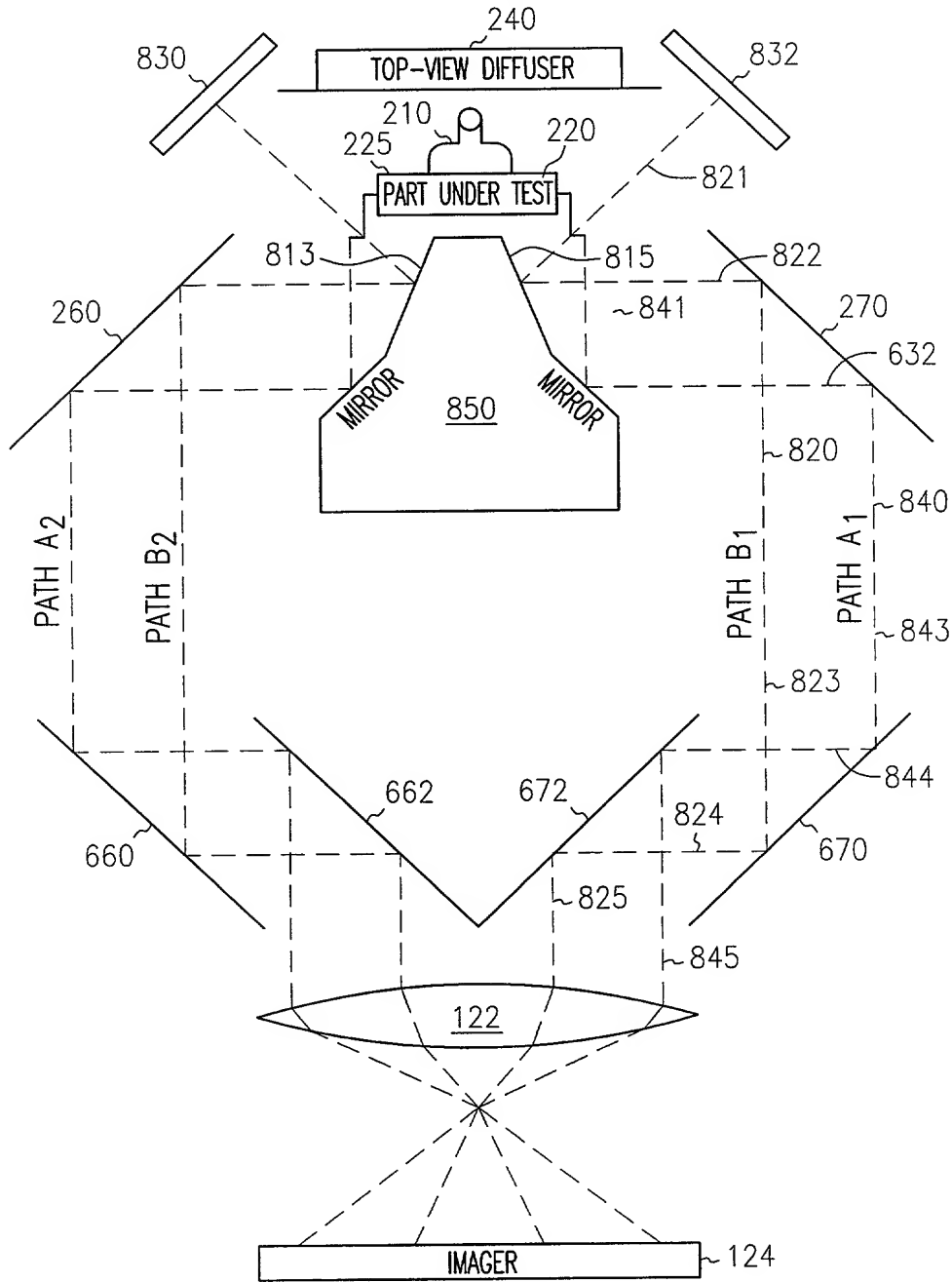


FIG. 8A

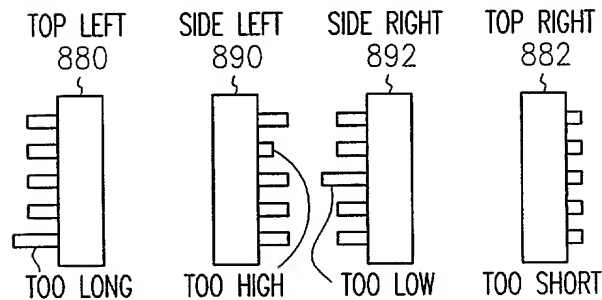


FIG. 8B

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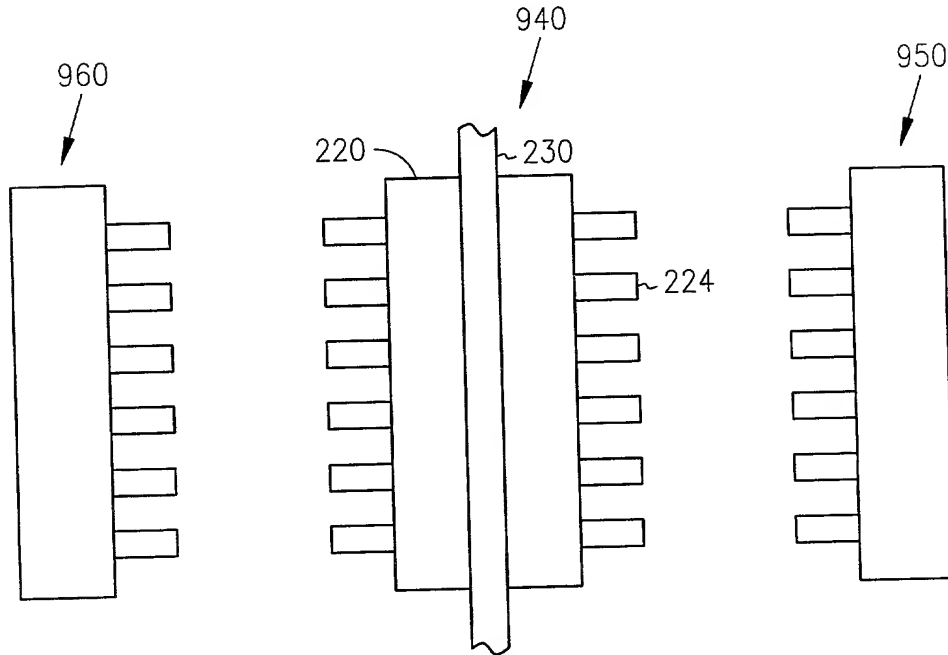


FIG. 9

10099684-101500

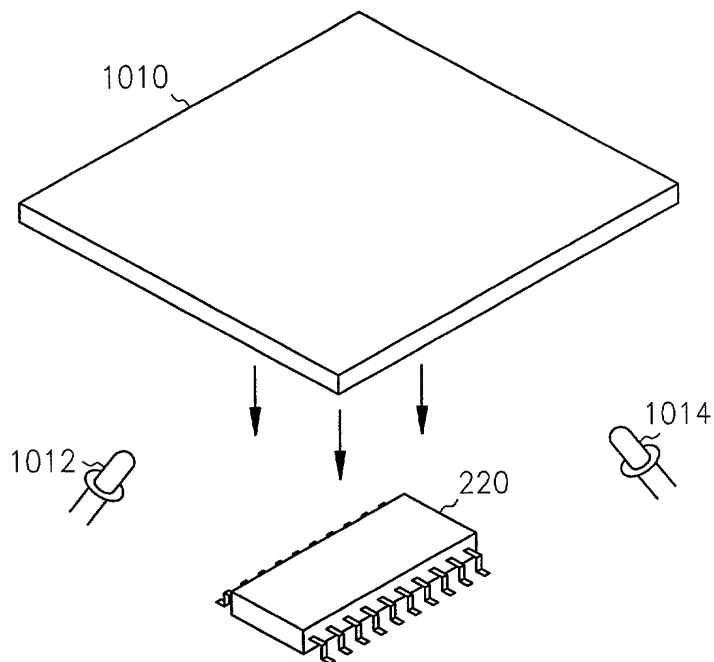


FIG. 10A

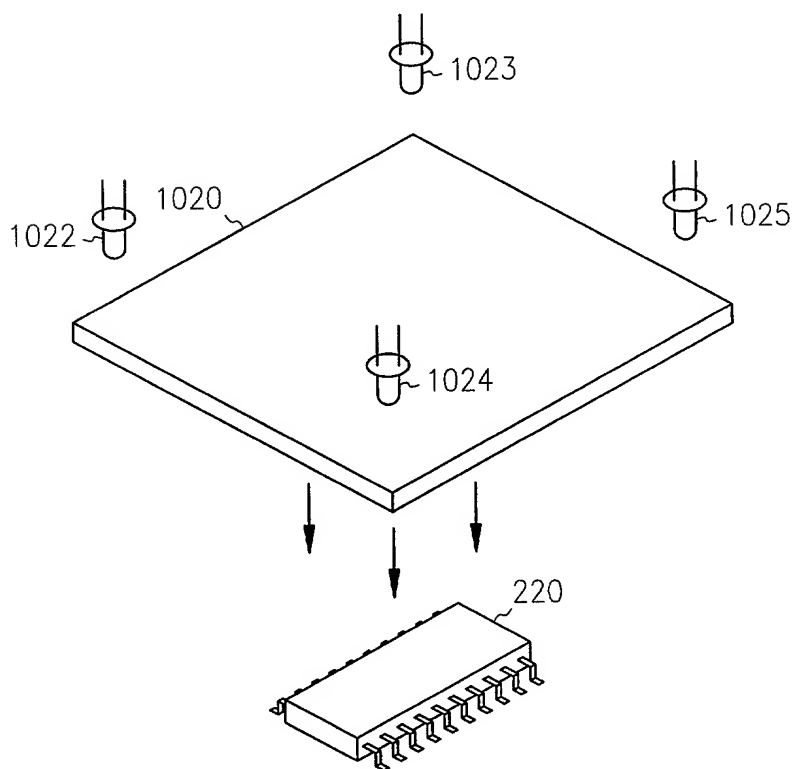


FIG. 10B

10099684-101502

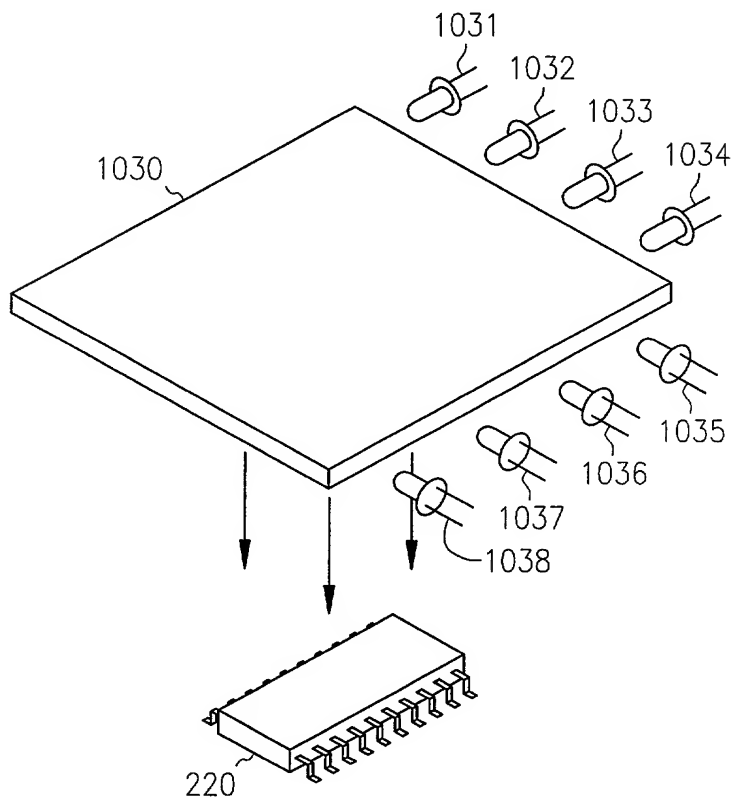


FIG. 10C

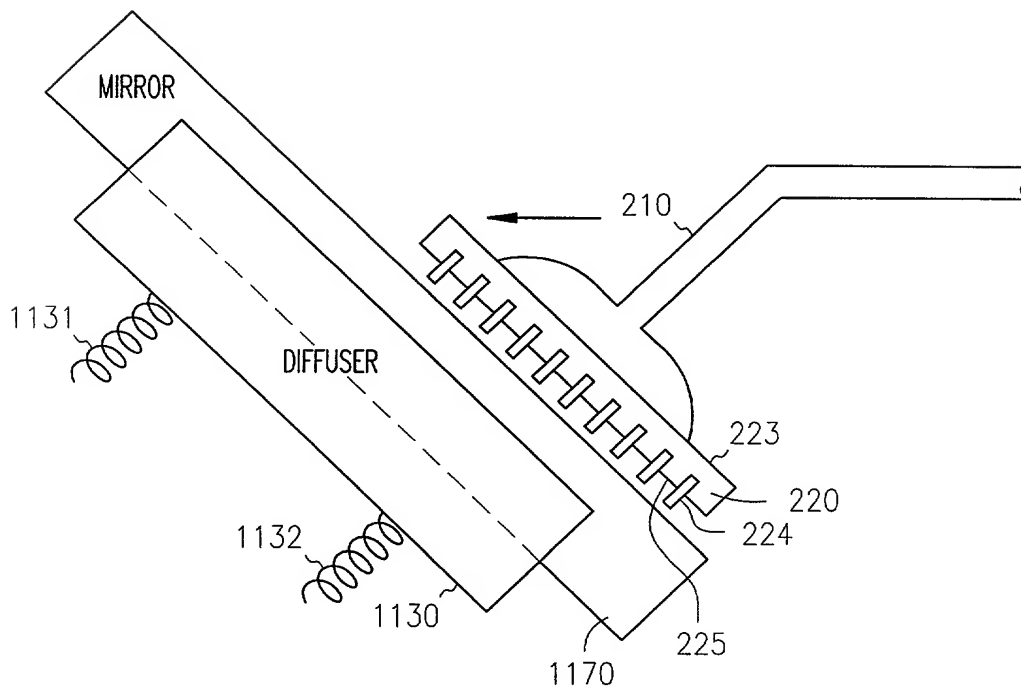


FIG. 11A

1099684-105107

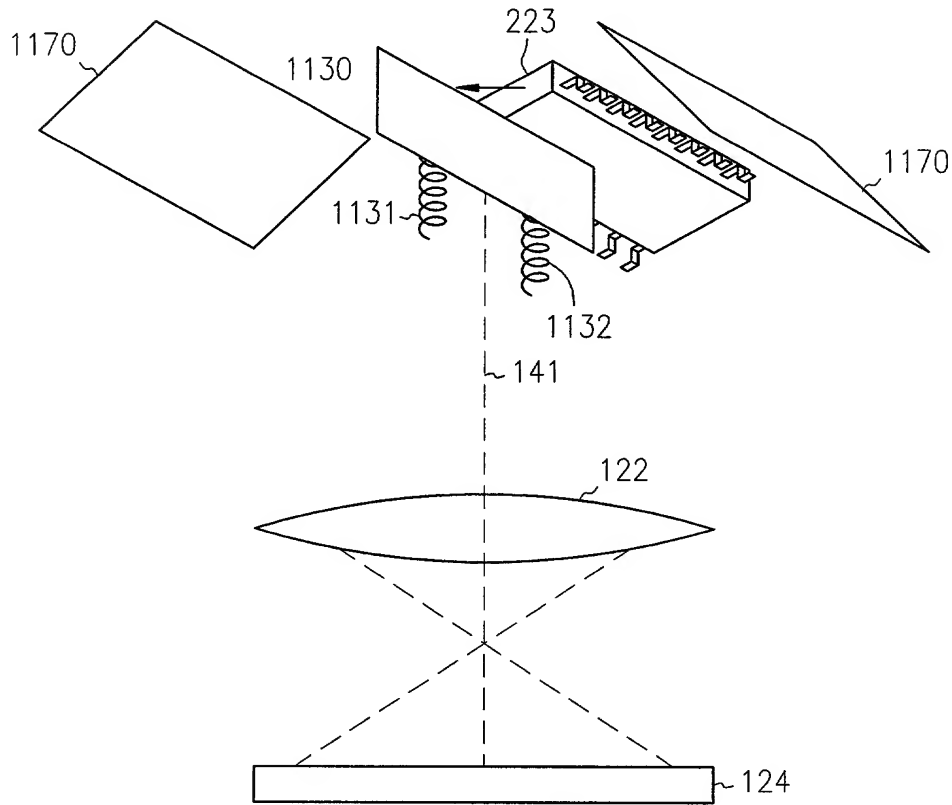


FIG. 11B

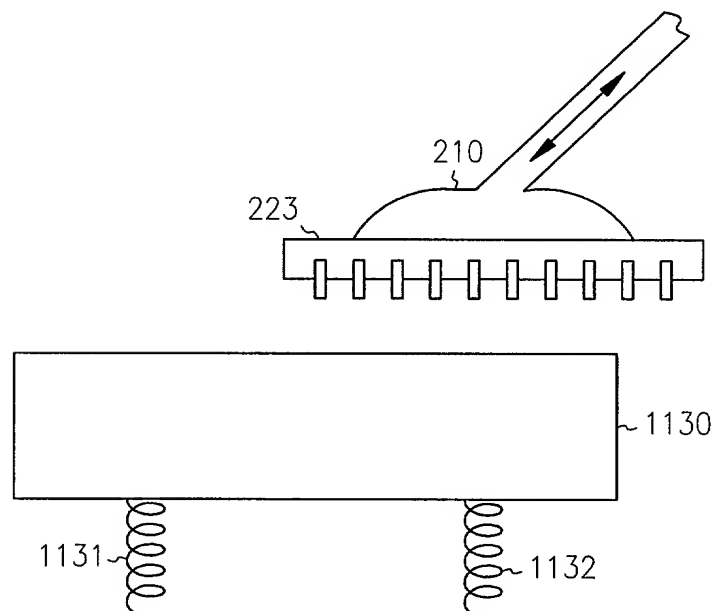


FIG. 11C

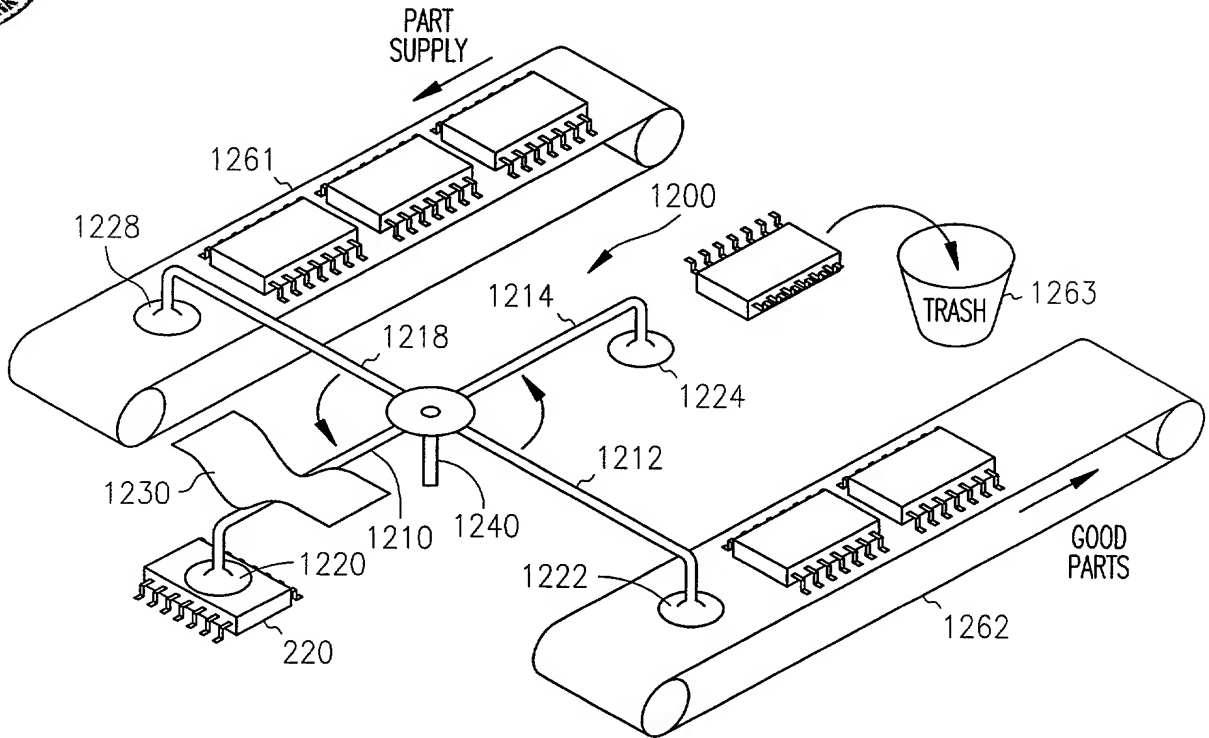


FIG. 12A

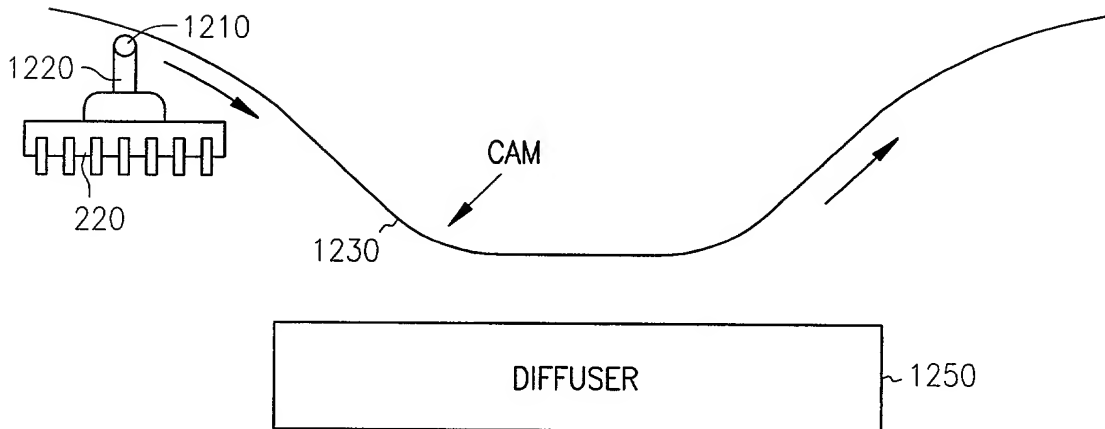


FIG. 12B

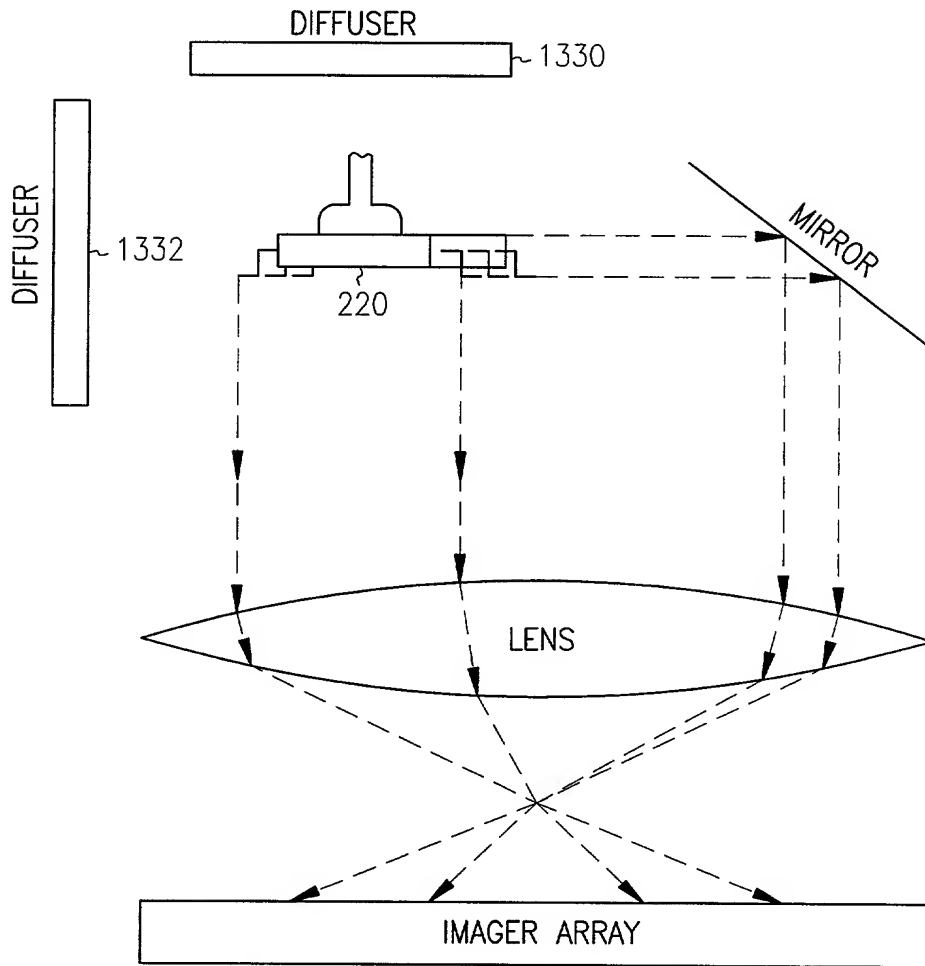
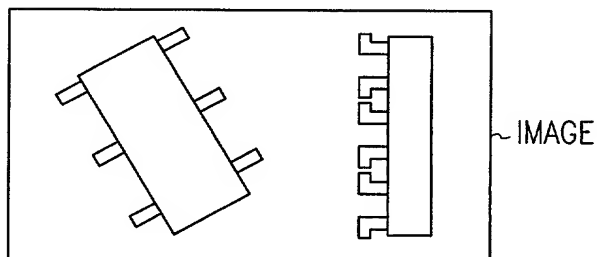


FIG. 13A



OBJECT PART SKEWED SO ALL PINS
SEPARATELY IMAGED FROM SIDE VIEW

FIG. 13B

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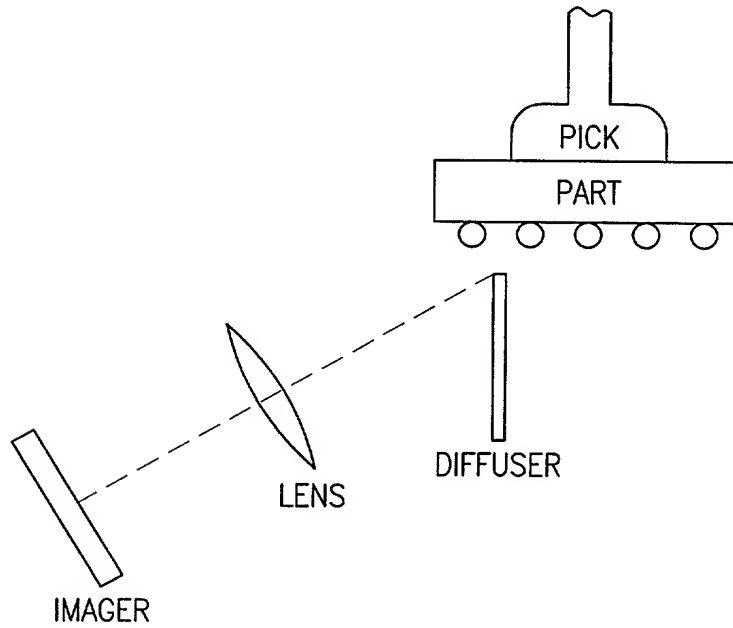


FIG. 14

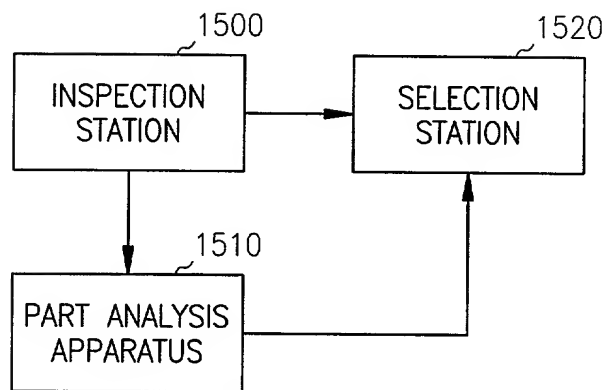


FIG. 15

10099684-101502